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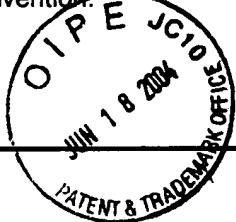
TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920040003US1 (17392)

In Re Application Of: **Paul D. Kartschoke, et al**

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/709,798	May 28, 2004	Unassigned	Unassigned	Unassigned

Invention: **METHOD FOR PREVENTING CIRCUIT FAILURES DUE TO GATE OXIDE LEAKAGE**



Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

- 2 sheets of formal drawing(s) for this application.
- Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

William C. Roch
Signature

William C. Roch
Registration No. 24,972

Correspondence Address
Customer No.: 23389

Dated: June 15, 2004

I certify that this document and attached formal drawings are being deposited on 6/15/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

William C. Roch
Signature of Person Mailing Correspondence

William C. Roch
Typed or Printed Name of Person Mailing Correspondence